

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Sachiko HATTORI

Art Unit: Unassigned

Application No. Unassigned

Examiner: Unassigned

Filed: October 2, 2003

For: METHOD FOR BURYING RESIST AND
METHOD FOR MANUFACTURING
SEMICONDUCTOR DEVICE

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Prior to the examination of the above-identified patent application, please enter the following amendments and consider the following remarks.